

FIG. 2-1

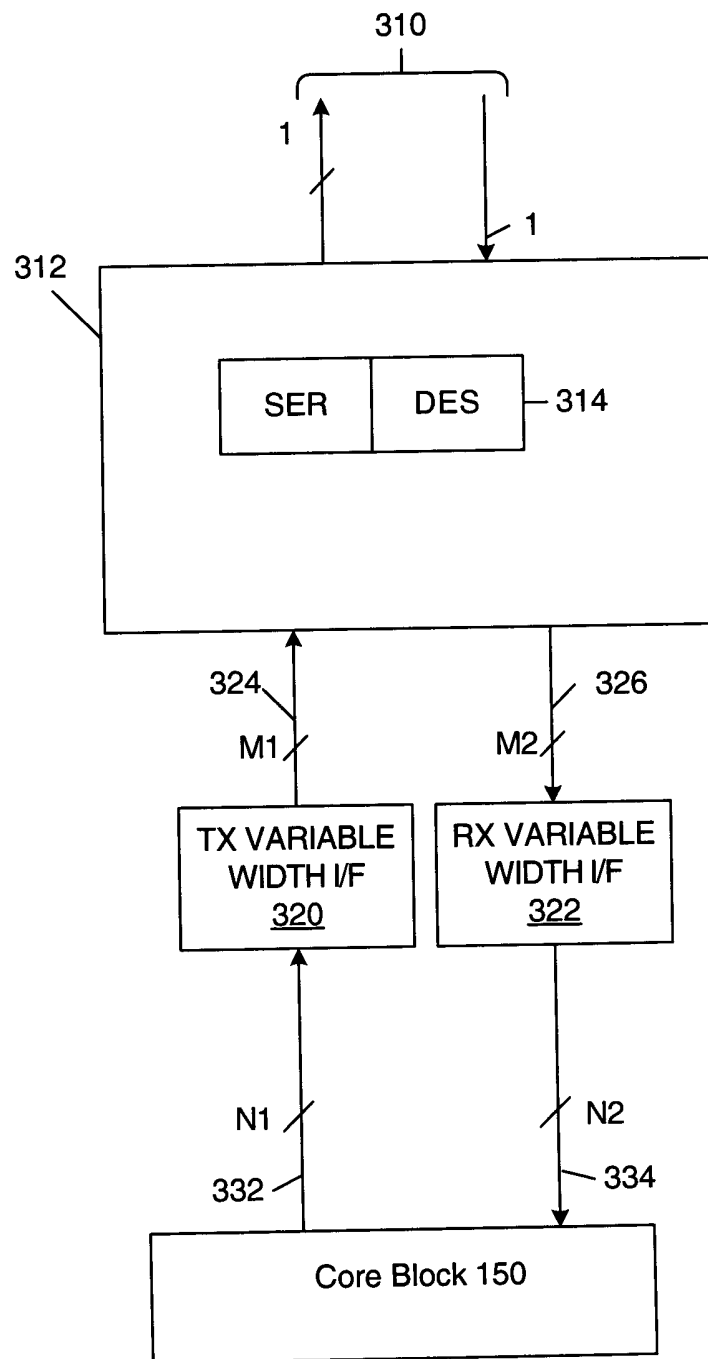


FIG. 2-2

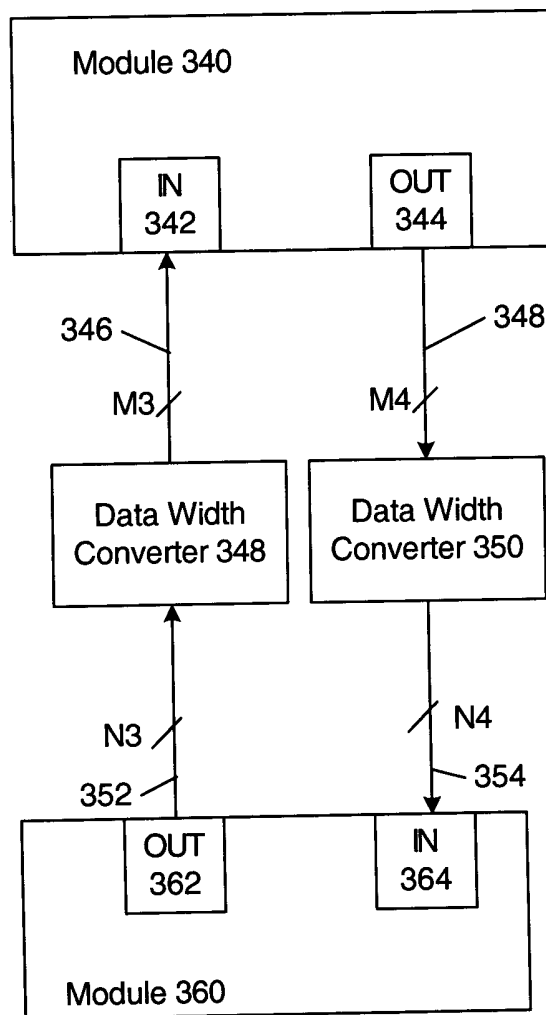
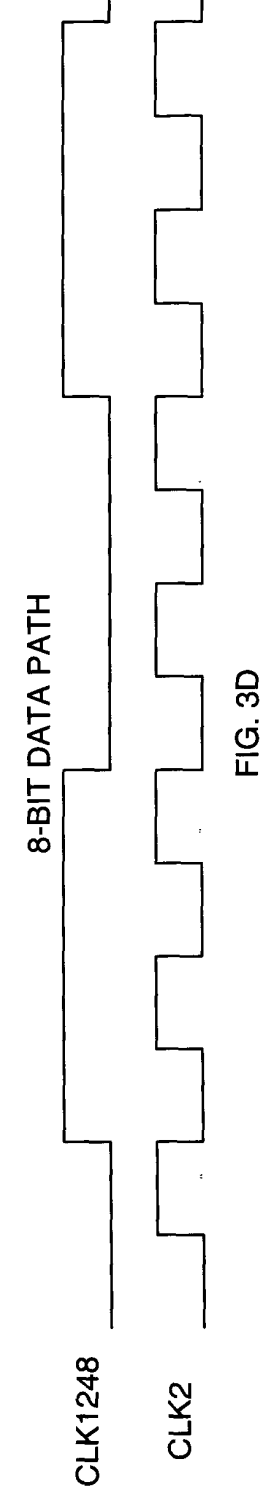
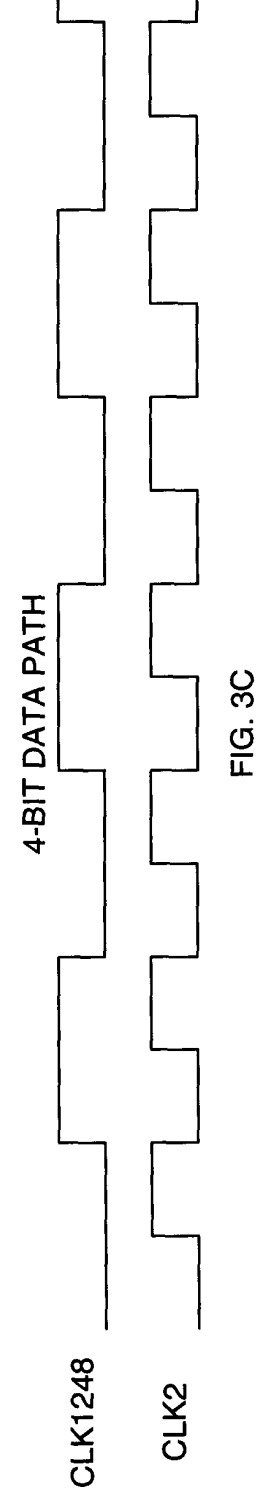
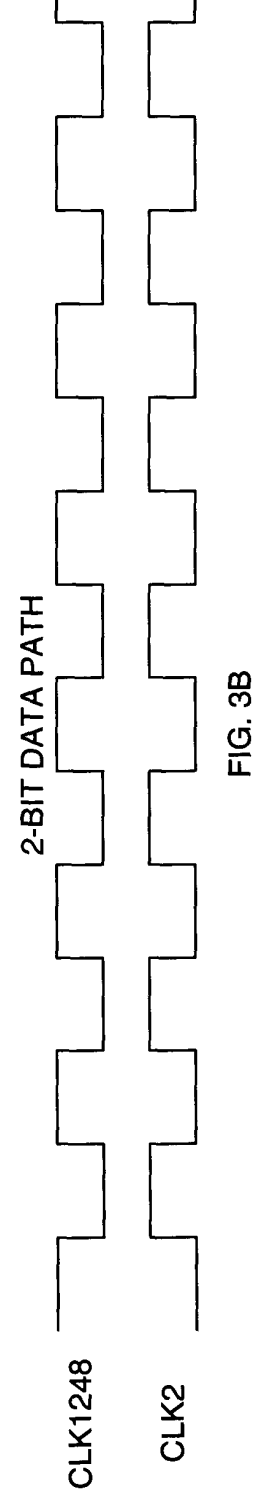
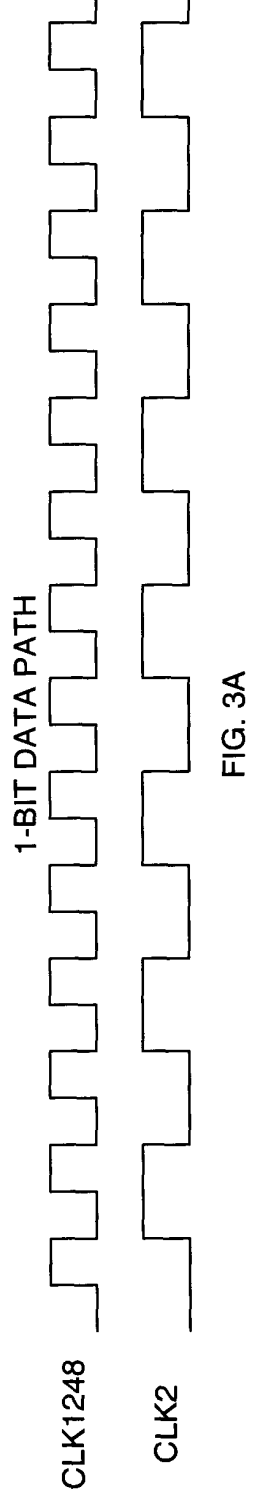


FIG. 2-3



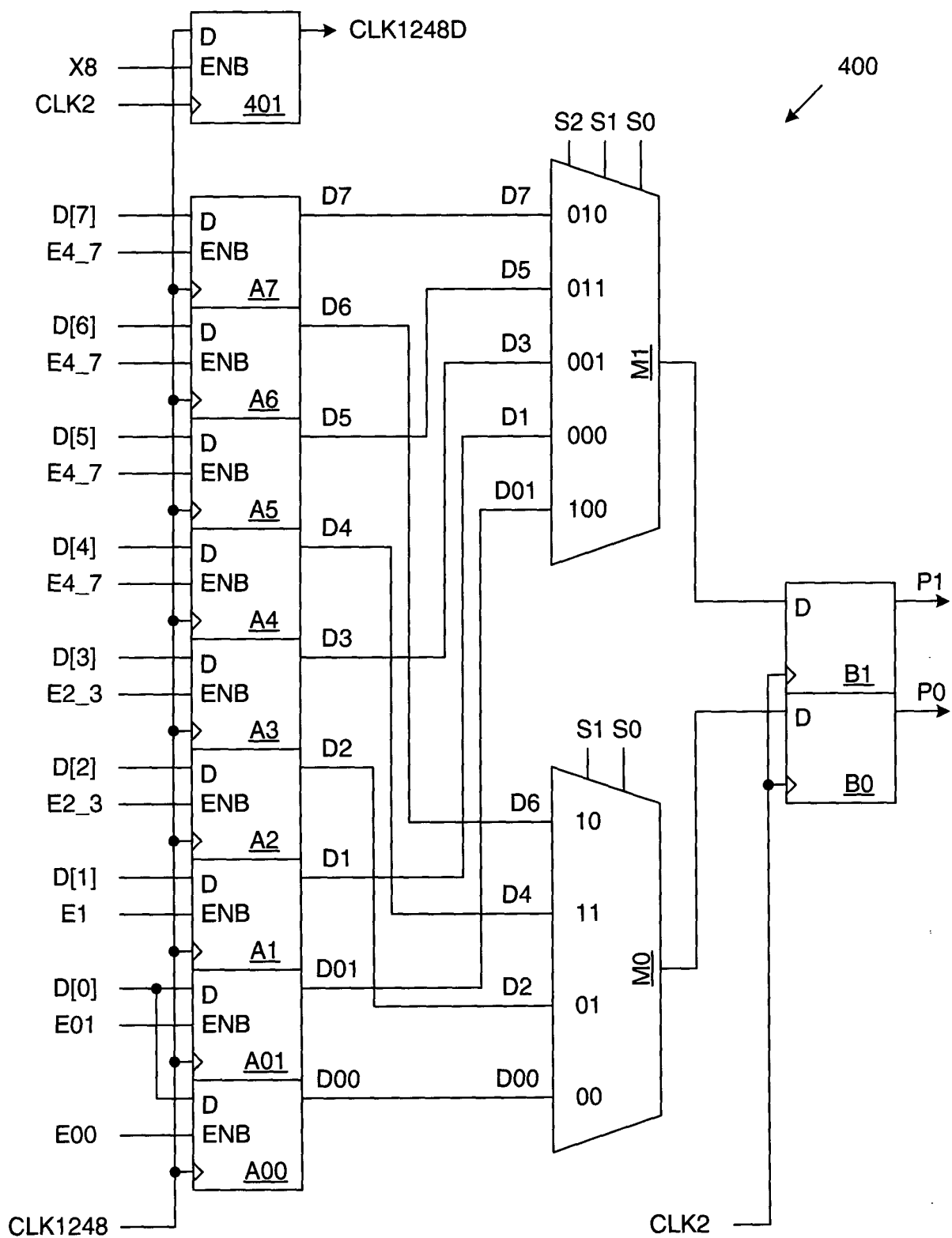


FIG. 4

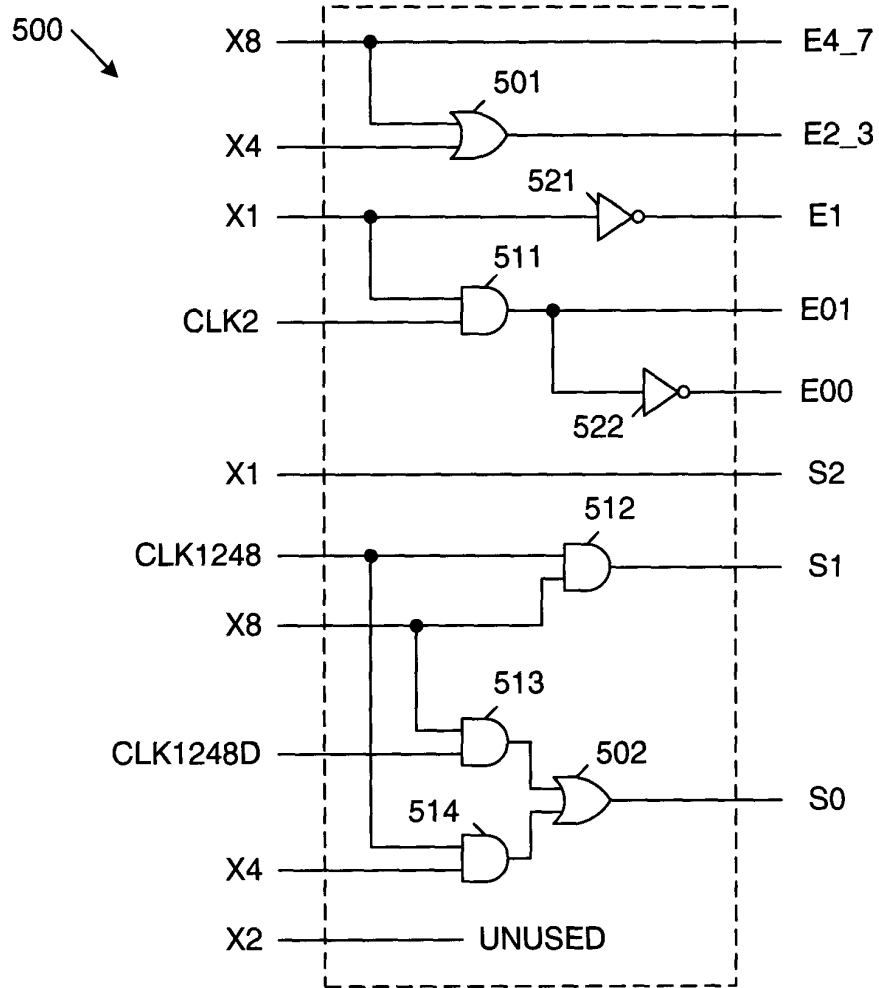


FIG. 5

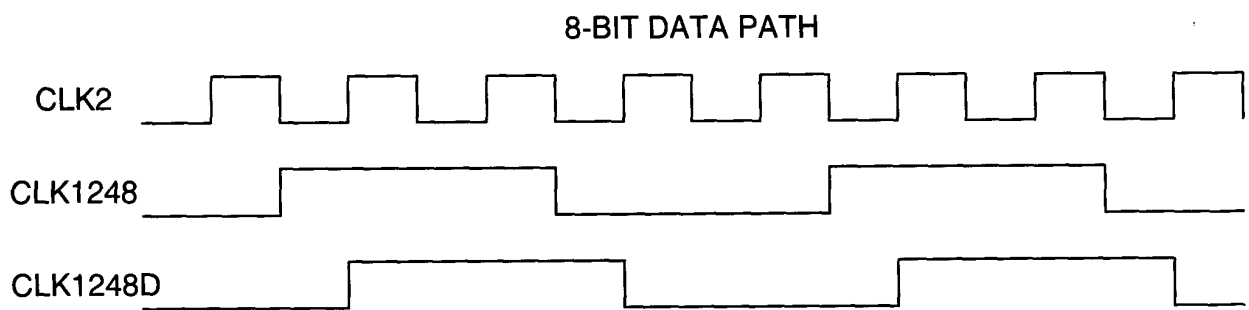


FIG. 6

# 1-BIT DATA PATH

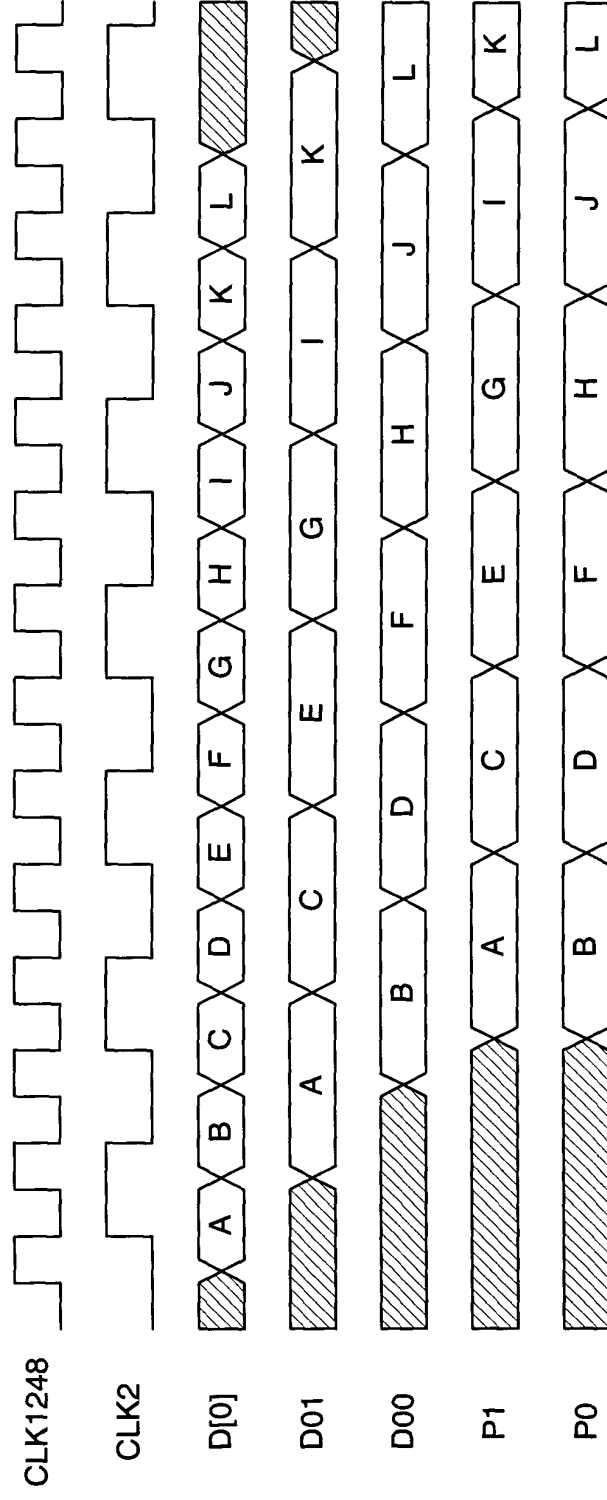


FIG. 7A



# 2-BIT DATA PATH

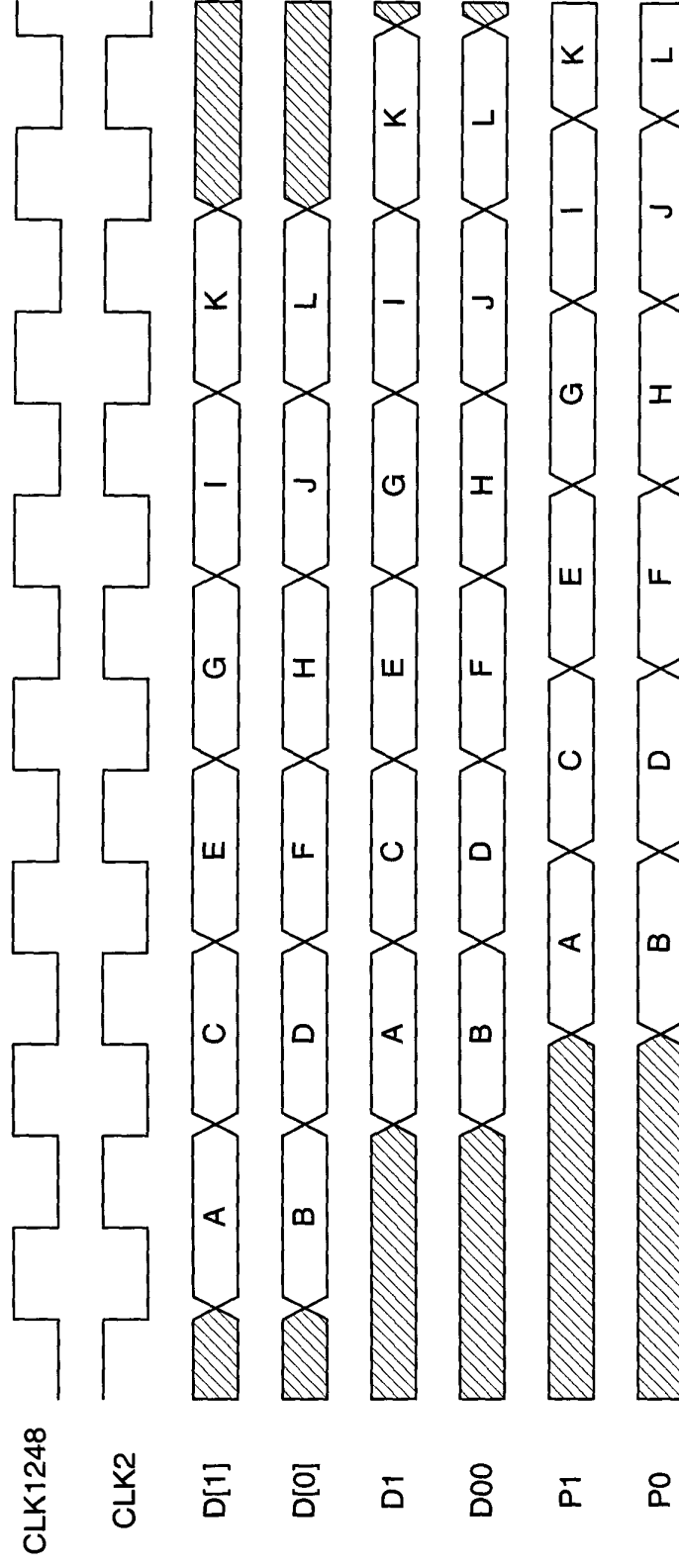


FIG. 7B

# 4-BIT DATA PATH

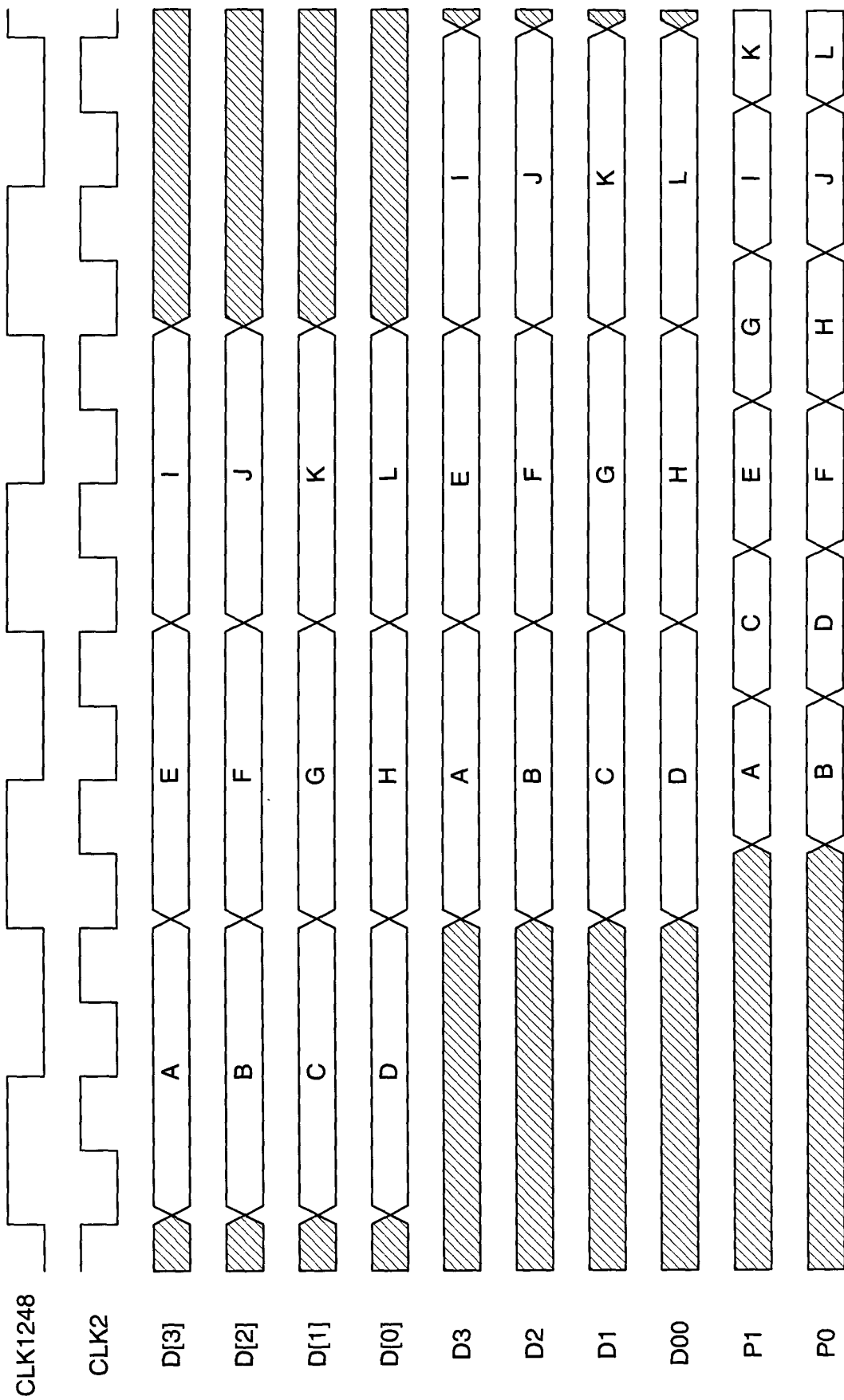


FIG. 7C

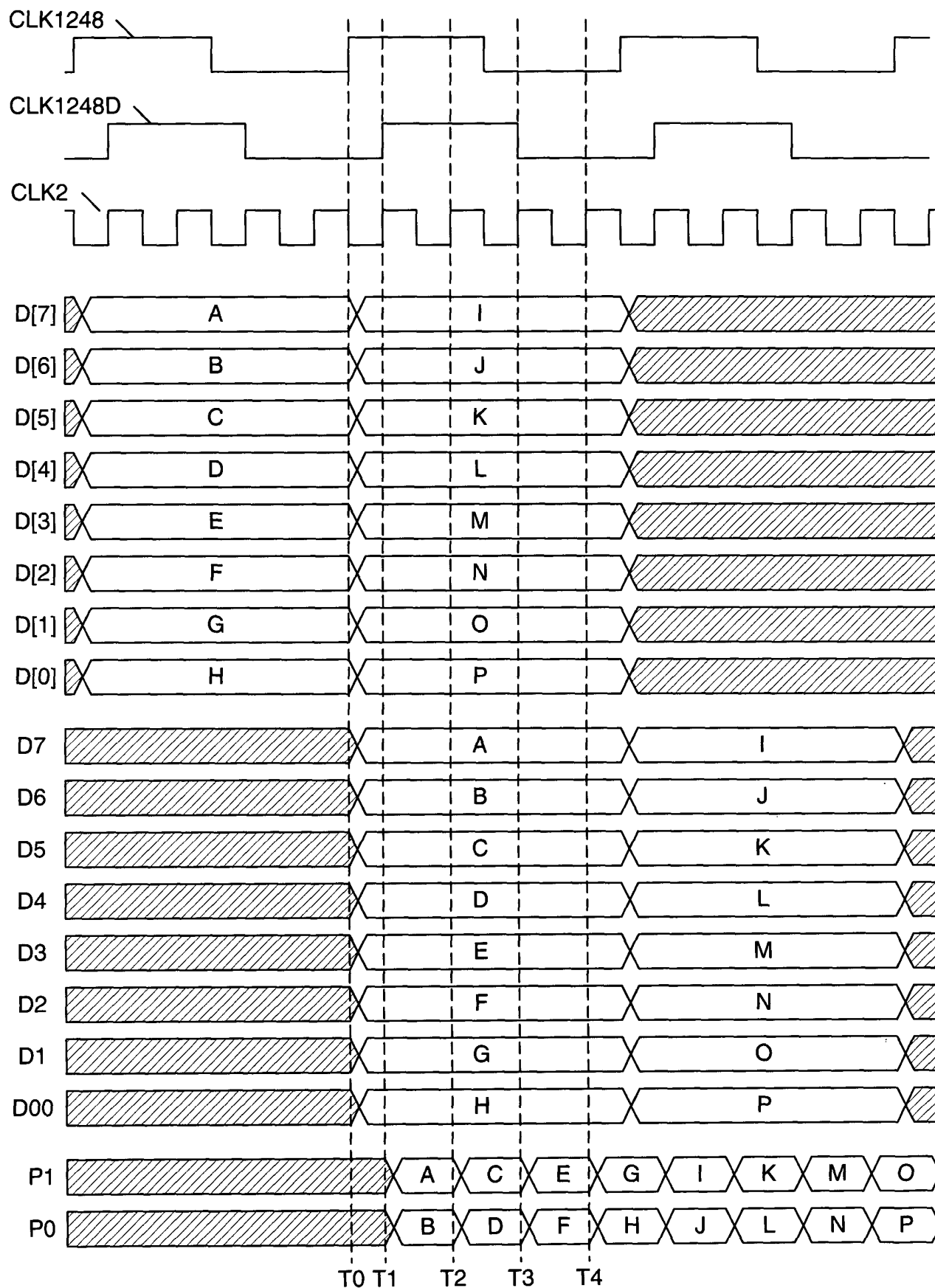


FIG. 7D

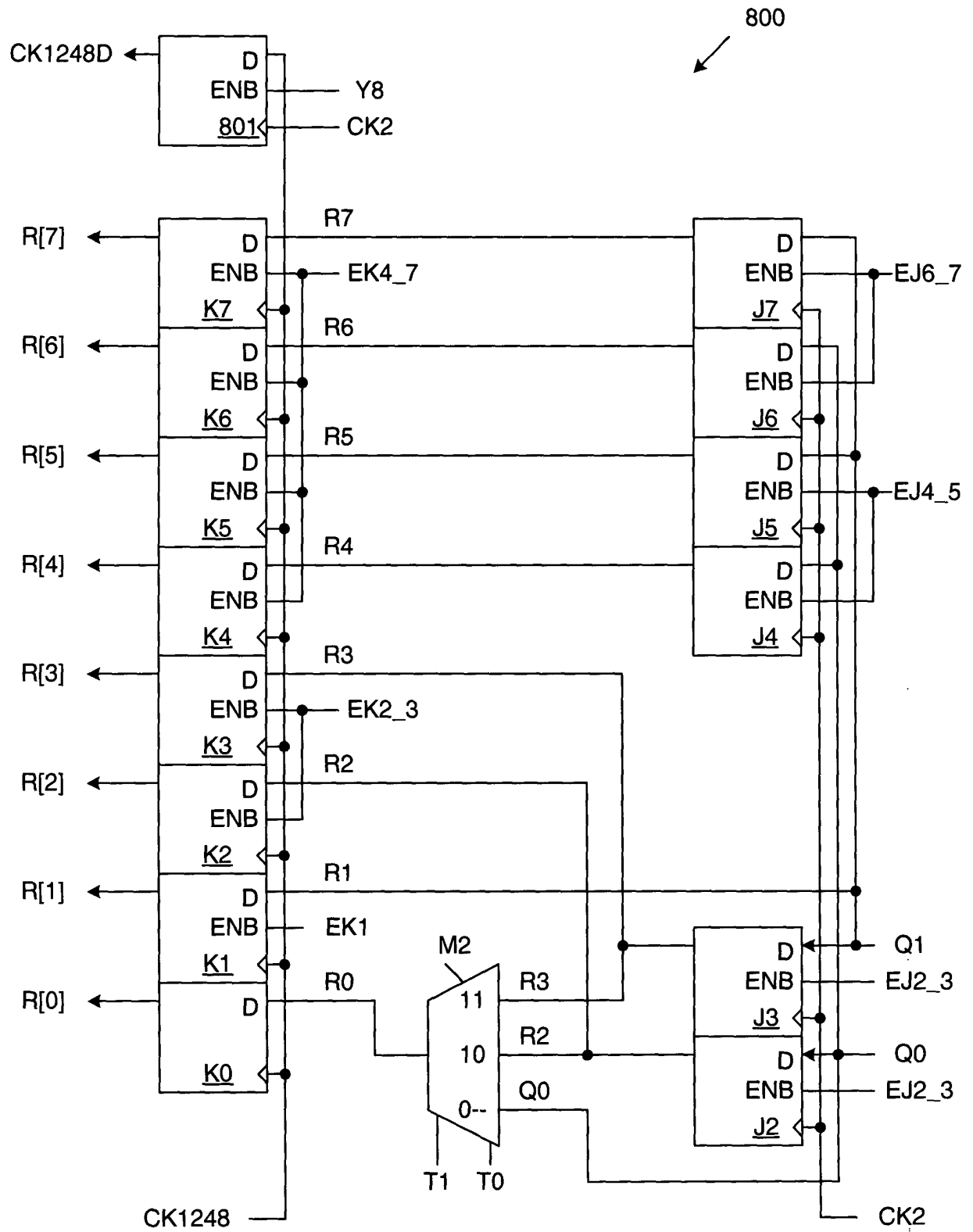


FIG. 8

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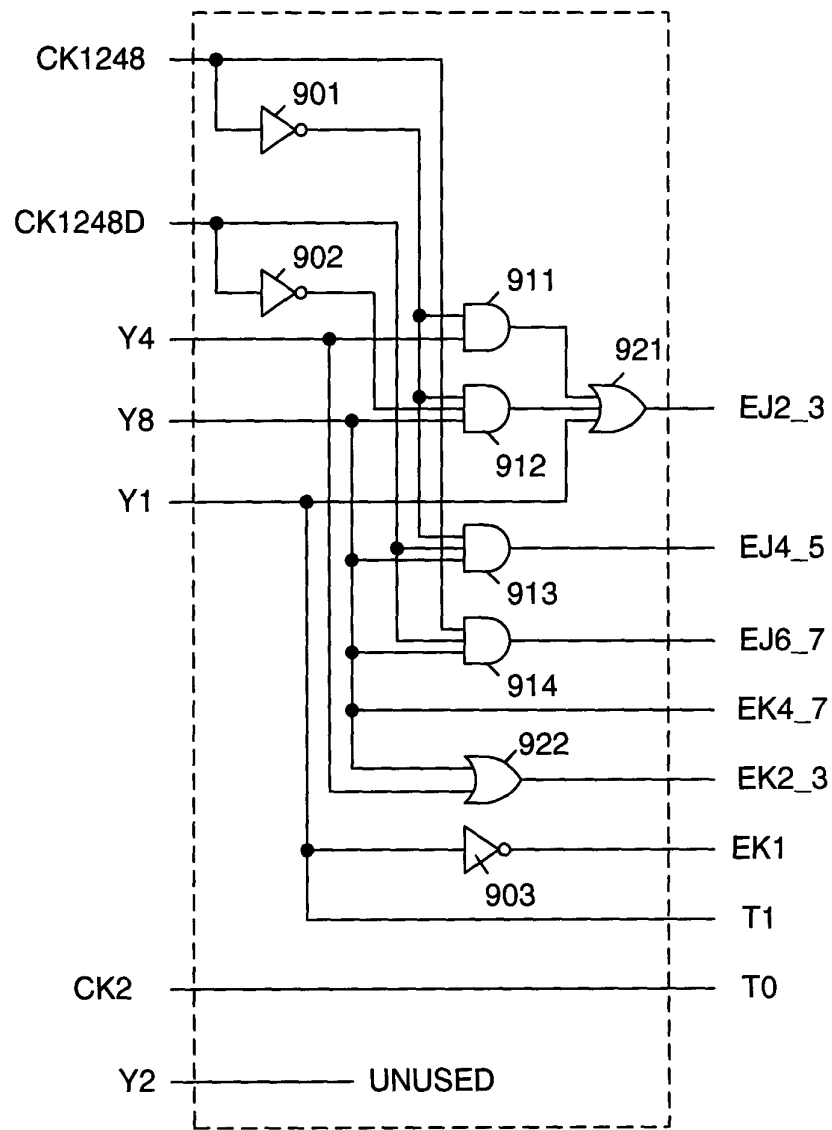


FIG. 9

# 1-BIT DATA PATH

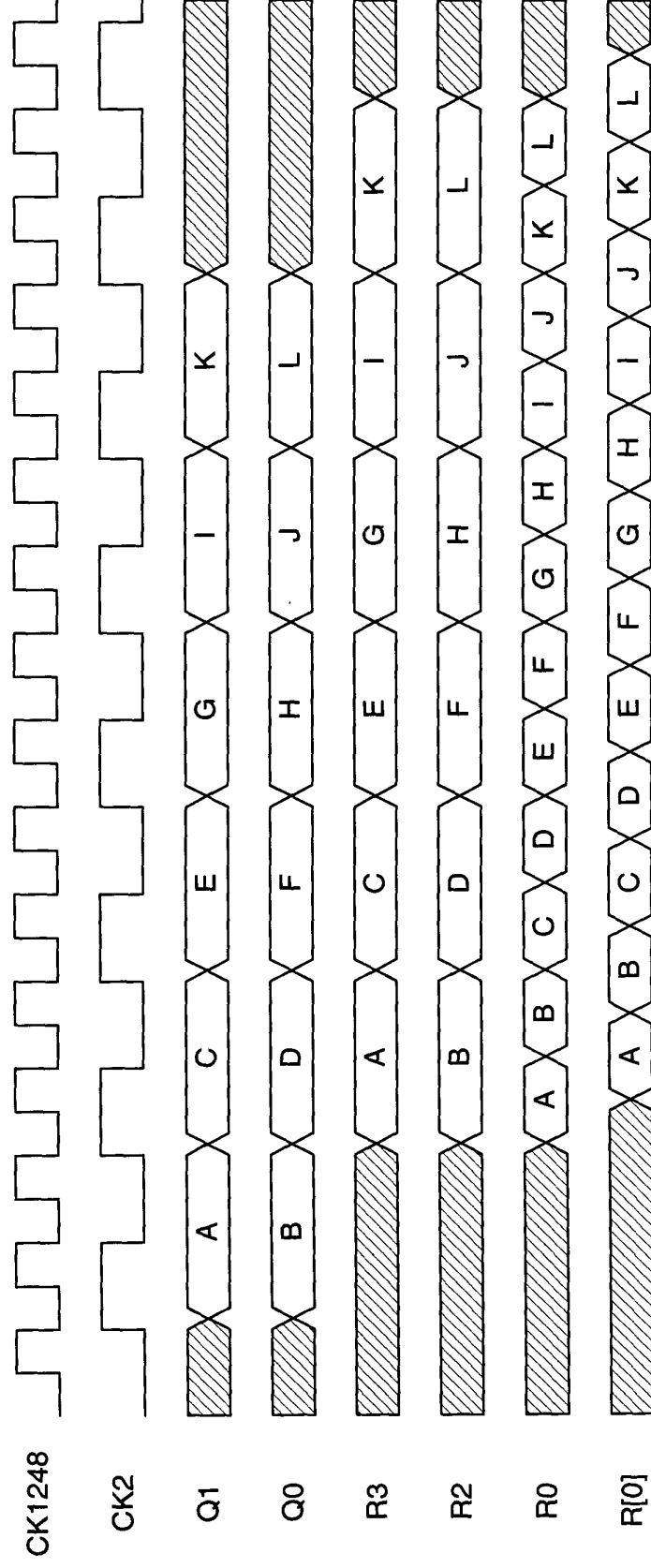


FIG.10A

# 2-BIT DATA PATH

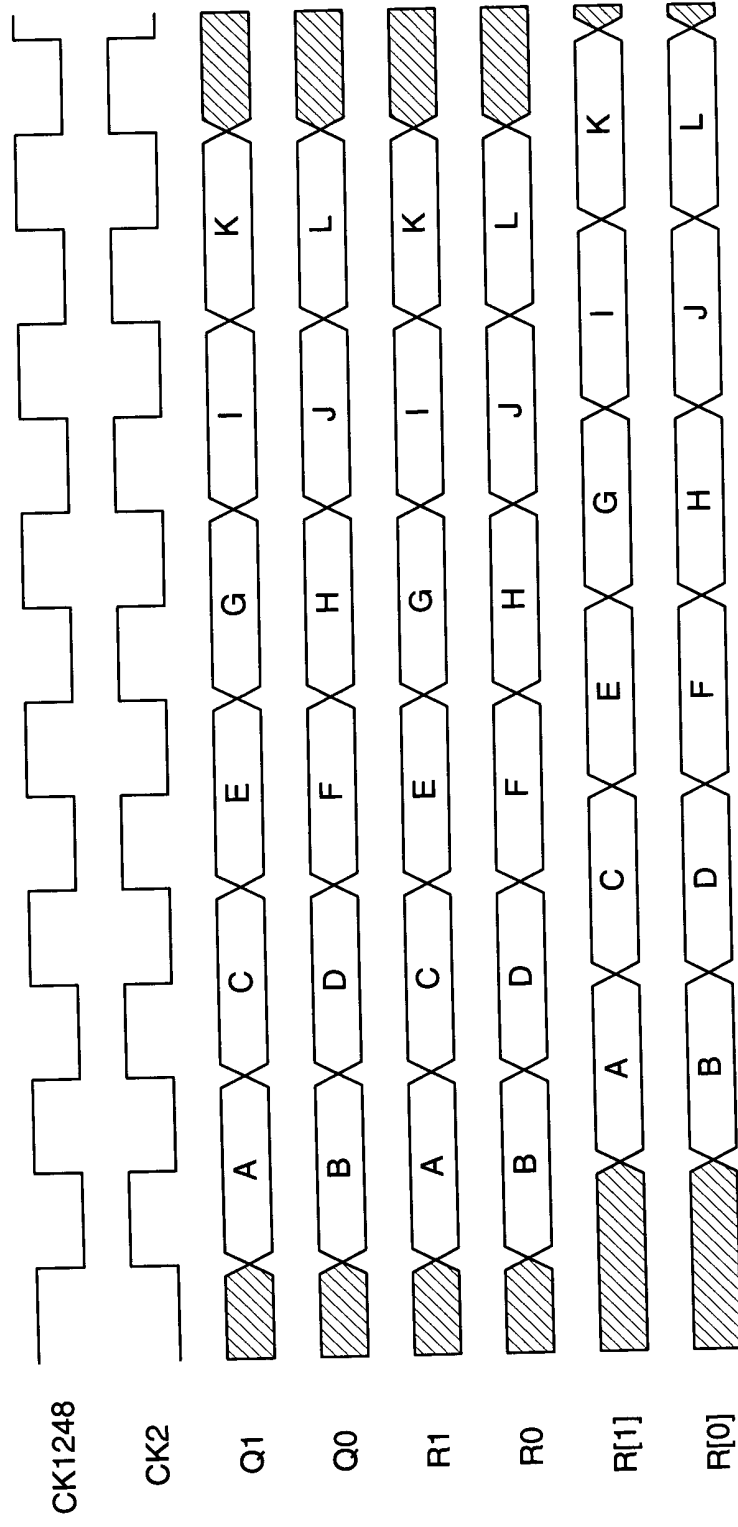


FIG. 10B

# 4-BIT DATA PATH

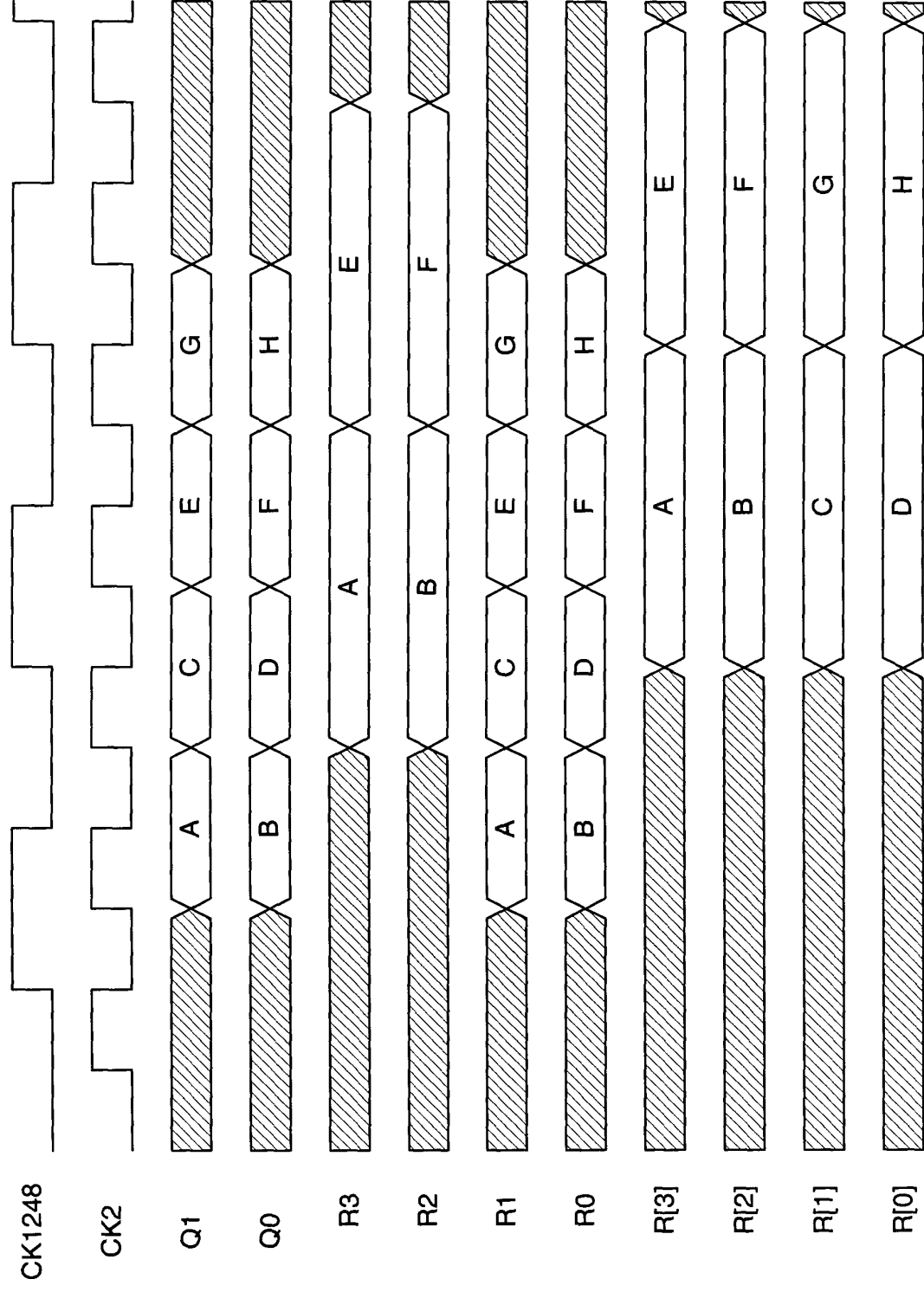


FIG. 10C



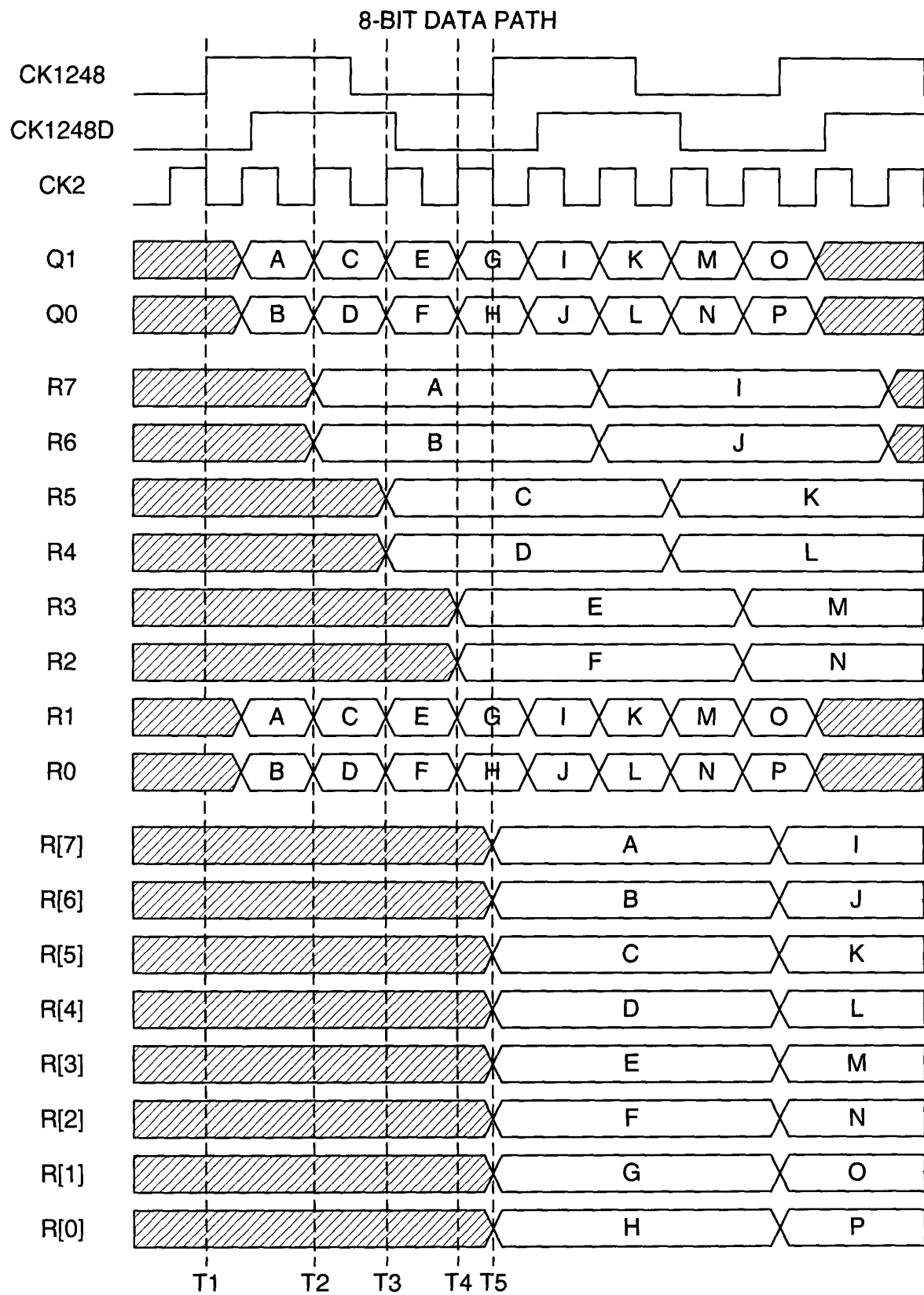


FIG. 10D

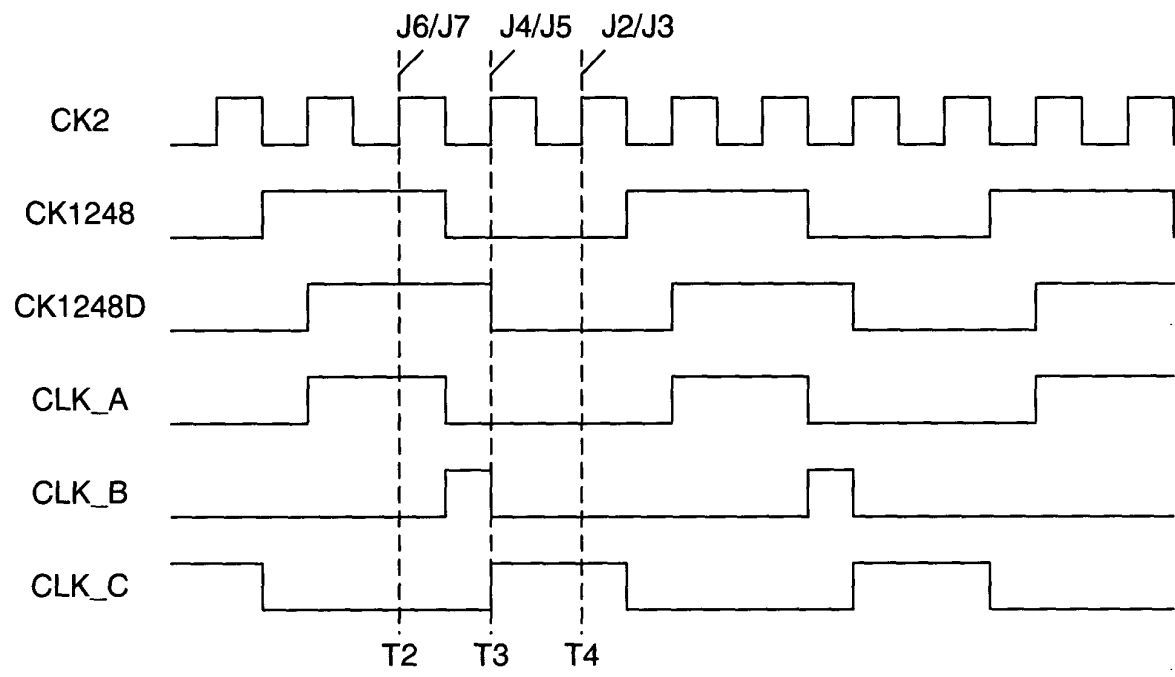


FIG. 11